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Multistage frequency conversion

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The present invention relates to a receiver using multistage frequency conversion. The invention further relates to a transmitter using multistage frequency conversion and to a transceiver comprising such a receiver and transmitter. The invention also relates to a method for multistage frequency conversion of a radio frequency signal.

A receiver using multistage frequency conversion is known from the US patent 6,282,413 B1. Shown is a receiver for down-converting a radio frequency signal using two separate frequency down conversion stages. Each of those stages is comprising a mixer. The corresponding mixing signals used by those mixers are generated by a tunable oscillator. However, since the tuning range of such a tunable local oscillator is limited per se, the corresponding receiving bandwidth i.e. the bandwidth from which the receiver can receive signals, is also limited

To this end, the receiver for receiving a radio frequency signal having a center frequency that is comprised in one of at least two frequency bands, comprising:

- oscillating means for generating a first mixing signal having a first frequency;
- a frequency divider arranged to derive a second mixing signal from the first mixing signal;
- a first mixer arranged to down-convert the radio frequency signal to a first lower frequency signal using the first mixing signal; and
- a second mixer arranged to down-convert the first low frequency signal to a second lower frequency signal using the second mixing signal;

in which a division factor of the frequency divider and a ratio between the center frequency and the first frequency are determined by the one of at least two frequency bands.

In the invention as claimed, the ratio between the frequency of the first mixing signal and the center frequency such as a carrier frequency, is determined by the frequency band comprising the center frequency. According to the present invention the oscillating means are arranged to generate the first mixing signal. The second mixing signal is derived from the first signal by using a frequency divider which has a division factor that is also determined by the frequency band comprising the center frequency. Therefore, the

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frequencies of the mixing signals are no longer fixed but are made variably dependent on the center frequency. Herewith the receiving bandwidth can advantageously be increased without having to increase the corresponding tuning range of the oscillating means. In addition, given a certain receiving bandwidth the present invention can advantageously be used for reducing the tuning range of the oscillating means without reducing the receiving bandwidth as such.

In a further embodiment according to the present invention the receiver is comprising a phase shifter to shift the phase of the second mixing signal which can be used for the down-conversion of quadrature signals such as I-Q signals.

These and other aspects of the invention will be further elucidated by means of the following drawings.

Fig. 1 shows some examples of frequency ranges that are used for wireless LAN applications.

Fig. 2 shows a first embodiment of a receiver according to the present invention.

Fig. 3 shows a second embodiment of a receiver according to the present

Fig. 4 shows an embodiment of a transmitter according to the present

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Fig. 5 shows an embodiment of a transceiver according to the present

Fig. 6 shows a flowchart showing the steps for multistage frequency conversion according to the present invention.

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Fig. 1 shows by means of example some frequency bands used with wireless LAN applications. As can be observed, some frequency bands are adjacent to others. See for example, USA WLAN and the frequency band for Automotive Telematics in the USA.

Fig. 2 shows a receiver according to the present invention. Shown are first and second mixers 12 and 16 which are coupled via low-pass filter 14 for removing unwanted spectral components from the first lower frequency signal 15. The receiver further comprises oscillating means 20 which may comprise a PLL or a free running oscillator for generating the first mixing signal. The embodiment further comprises frequency divider 22 for deriving

the second mixing signal 13 from the first mixing signal 11. Both the division factor of the frequency divider and the ratio between the frequency of the first mixing signal 11 and the center frequency are dependent on the frequency band comprising the center frequency. It is to be noted that the wording center frequency also comprises a carrier frequency. The achievable reduction in tuning range is illustrated below. By means of example, the frequency of the first mixing signal 11 has a ratio of N/(N+1) to the carrier frequency. The frequency divider 22 has a division factor N. Therefore, the frequency of the second mixing signal equals 1/(N+1) times the center frequency. N can assume any integer number For N=2, the first mixing signal would have a frequency of 2/3 times the center frequency whilst the second mixing signal would have a frequency of 1/3 times the center frequency. In table 1, the tuning range is calculated for different values of N. Nevertheless, N is fixed for the entire receiving bandwidth that ranges from 4900 to 5925 MHz. The tuning range of the oscillating means 20 is expressed in terms of a relative bandwidth (%) which can be calculated as: (Fmax-Fmin)/((Fmax+Fmin)/2)*100%.

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Center frequency	N	1st mixing signal	Tuning range
(MHz)		(MHz)	(%)
4900 - 5925	2	3267 – 3950	18.9
4900 - 5925	3	3675 – 4444	18.9
4900 - 5925	4	3920 – 4740	18.9

Table 1: Tuning range for fixed N.

In table 2, the value of N is made dependent on the frequency band comprising the center frequency.

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Center frequency (MHz)	N	1 st mixing signal (MHz)	Tuning range (%)
4900 – 5266	3	3675 – 3950	
5267 – 5925	2	3511 – 3950	11.5

Table 2: Tuning range for variable N.

In this case the oscillator means 20, only need to cover the frequency range from 3511 to 3950 MHz. This represents a tuning range of 11.8 % which is about 1.5 times lower compared to the first situation. Although in the above example the ratio between the

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first frequency and the center frequency equals N/(N+1), other ratio's and division factors are equally possible. For a zero-IF receiver for example, a ratio of N/(N-1) between the frequency of the first mixing signal 11 and the center frequency of the input signal 10 can be used as well. In this case, by using frequency divider 22 having a division factor N, the frequency of the second mixing signal 13 becomes 1/(N-1) times the center frequency. In general, for zero-IF, the sum of the frequencies of the mixing signals 11 and 13 must equal the center frequency. For non-zero IF however, the ratio and the division factor should be chosen such that the sum of the frequencies of mixing signals 11 and 13 does not equal the center frequency.

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Fig. 3 shows an embodiment of a receiver according to the present invention wherein mixers 30 and 32 are used for down-converting quadrature signals for use in for example I-Q demodulators. In order to down-convert the quadrature signals the receiver comprises a phase shifter 34 for shifting the phase of the second mixing signal.

Fig. 4 shows an embodiment of a transmitter according to the present invention. Shown are mixers 57 and 59 for up-converting lower frequency signal 10 to a radio frequency signal 53. To this end, mixers 57 and 59 make use of mixing signals 54 and 55. Mixing signal 54 is derived from mixing signal 55 using frequency divider 61 which has a programmable division factor. Mixing signal 55 is generated using oscillating means 56 which can e.g. be PLL based or can make use of a free running oscillator. The division factor of the frequency divider 61 and the ratio between the frequency of the mixing signal 55 and the center frequency of signal 53 are equally determined by the frequency band comprising the center frequency.

Fig. 5 shows a transceiver 64 comprising transmitter 61 and receiver 62. Receiver 62 receives input signal 10 from antenna 67 and frequency down-converts the input signal 10 to the lower frequency signal 18 which can be either a zero-IF or a near zero-IF signal. Signal 65 is obtained from the lower frequency signal 18 after being processed in processing means 63. Processing means 60, process signal 66 into the lower frequency signal 50. This signal is subsequently frequency up-converted by means of transmitter 61 and transmitted through antenna 67.

Fig. 6 shows a flowchart comprising four steps S1,S2,S3 and S4 for frequency down converting a signal 10 using multistage frequency conversion. In step S1 a first mixing signal 11 is generated having a ratio to the center frequency, which ratio is determined by the one of at least two frequency bands that is comprising the center frequency. In step S2 a second mixing signal 13 is derived from the first mixing signal by using a frequency divider

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22 having a division factor which is determined by the one of at least two frequency bands. In step S3, the radio frequency signal 10 is down-converted into a first lower frequency signal using the first mixing signal. Finally in step S4, the first lower frequency signal is frequency down-converted into a second lower frequency signal 18 using the second mixing signal 13.

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It is to be noted that the above-mentioned embodiments illustrate rather than limit the invention, and that those skilled in the art will be able to design many alternative embodiments without departing from the scope of the appended claims. The embodiments can be realized in either the analogue or digital domain using analogue and digital components. The word "comprising" does not exclude the presence of elements or steps other than those listed in a claim. The word "a" or "an" preceding an element does not exclude the presence of a plurality of such elements. The mere fact that certain measures are recited in mutually different dependent claims does not indicate that a combination of these measures cannot be used to advantage.